


☐ Drafts

☐ Pending

☒ Active

- ☒ L1: (1009) ((dynamic near3 mode) and
- ☒ L2: (2) ((dynamic near3 mode) with (
- ☒ L3: (39) ((static near3 mode)with (p
- ☒ L4: (0) ((static near3 mode) same (not

☐ Failed

☒ Saved

- ☒ (22) ((array bank block) and ((signal
- ☒ (4) ((test\$3 with dedicat\$3 with line
- ☒ (4) ((array bank block) and ((signal
- ☒ (22) ((static adj2 operation) (static adj2 mode)) near5 (precharg\$3 pre-charg\$3)
- ☒ (4) ((dynamic adj2 operation) (dynar
- ☒ (67) ((dynamic adj2 operation) (dynar

☒ Favorites

☒ Tagged (17)

☐ UDC

☐ Queue

DB: USPAT;US-PGPUB;EPO;JPO;DERWENT;IBM;TDB

Default operator: DR

☒ Plural

☒ Highlight all hit terms initially

((static near3 mode)with (precharg\$3 pre-charg\$3)

☒ BRS form ☒ IS&R form ☒ Image ☒ Text ☒ HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020196057 A1	20021226	123	Data output circuit with reduced output noise	327/108		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20020084835 A1	20020704	23	Semiconductor device with reduced leakage of current	327/544		
3	<input type="checkbox"/>	<input type="checkbox"/>	US 20020067647 A1	20020606	12	Semiconductor integrated circuit device	365/203		
4	<input type="checkbox"/>	<input type="checkbox"/>	US 6563730 B1	20030513	12	Low power static RAM architecture	365/154	365/189.01	
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6445222 B1	20020903	113	Data output circuit with reduced output noise	327/108	326/87; 327/112	
6	<input type="checkbox"/>	<input type="checkbox"/>	US 6163180 A	20001219	112	Data output circuit with reduced output noise	327/112	327/540; 327/541	
7	<input type="checkbox"/>	<input type="checkbox"/>	US 5948081 A	19990907	11	System for flushing queued memory write request	710/40	710/25; 710/26;	

☒ Hits ☐ Details ☒ HTML

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DB: USPAT;US-PGPUB;EPO;JPO;DERWENT;IBM_YDB

☒ Plurals
☒ Highlight all hit terms initially

Default operator: OR

```

((dynamic adj2 operation (dynamic adj2 mode: near8
(precharg93 pre-charg93) and (static adj2 operation
static adj2 mode: near8 precharg93 pre-charg93)

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	U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20020067647 A1	20020606	12	Semiconductor integrated circuit device	365/203		
2	<input type="checkbox"/>	<input type="checkbox"/>	US 5777491 A	19980707	21	High-performance differential cascode voltage	326/110	326/121; 326/17;	
3	<input type="checkbox"/>	<input type="checkbox"/>	US 4086500 A	19780425	7	Address decoder	326/108	326/98; 365/330.06	
4	<input type="checkbox"/>	<input type="checkbox"/>	US 4079332 A	19780314	5	High gain differential amplifier	330/253	330/360	

☒ Hits
☐ Details